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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

~~Applicant(s):~~ Dennis James Herrell

~~Title:~~ CROSSTALK CANCELLATION FOR INTEGRATED CIRCUIT
PACKAGE CONFIGURATION

~~Application No.:~~ 09/431,640

Filed: November 1, 1999

~~Examiner:~~ T. Tran

Group Art Unit: 2841

~~Atty. Docket No.:~~ 1001-0062

October 21, 2002

Box AF
COMMISSIONER FOR PATENTS
Washington, DC 20231

RESPONSE TO NON-FINAL OFFICE ACTION

Sir:

This is in response to the Office action dated June 19, 2002, having a shortened statutory period for response set to expire September 19, 2002. Please amend the above-identified application as follows:

AMENDMENTIn the Specification

Please amend the specification as follows:

56
D, 1
Although the design of transformer structures suitable to a given electrical assembly may vary depending on the characteristics or design of a particular electrical assembly, one advantageous configuration is particularly suitable for definition on a planar electrical assembly (e.g., on a board, card, package, or integrated circuit chip). In such configurations, a voltage plane is generally employed to reduce inductive coupling between traces 10, 20, 30, 40, and 50 on the electrical assembly. In one realization, apertures 112, 112A, 112B, 112C, 112D are defined in the voltage plane to provide the integrated transformer structures 110, 110A, 110B,